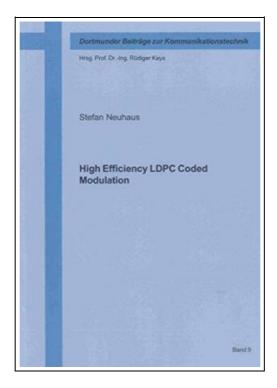
# High Efficiency LDPC Coded Modulation



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## HIGH EFFICIENCY LDPC CODED MODULATION



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Shaker Verlag Feb 2014, 2014. Buch. Condition: Neu. Neuware - The combination of low-density parity-check (LDPC) codes and higher-order modulation represents one of the most powerful digital transmission schemes for bandwidth-limited channels. Information transmission close to fundamental limits is enabled in this way. However, this only holds for large block lengths resulting in high latency as well as high complexity. Therefore, this thesis is dedicated to high efficiency LDPC coded modulation schemes that require a small number of channel uses to transmit a possibly large amount of information. For this, short LDPC codes with block lengths in the order of 102 - 103 bits and higher-order modulation are considered. First, bit-interleaved coded modulation (BICM) deploying (binary) irregular LDPC codes is discussed as one of the most common state-of-the-art transmission schemes for bandwidth-limited channels. A tool based on extrinsic information transfer (EXIT) functions is extended to analyze the combination of LDPC codes and higher-order modulation. Based on this, optimized interleavers are derived which efficiently adapt the unequal error protection features in both LDPC coding and modulation. In contrast to previous work in this field, an improved performance is achieved also for short codes. Next, symbol-based coded modulation is introduced which combines non-binary LDPC codes and higher-order modulation. Non-binary LDPC codes can be constructed with short block lengths and good performance. Moreover, the size of the finite extension field can directly be matched to the applied modulation order. By means of symbol-based mapping and demapping significant performance gains can be achieved. Furthermore, partial symbol mapping is introduced to reduce the overall complexity while retaining much of the good performance. Finally, a comprehensive evaluation of competitive approaches to LDPC coded modulation for practical block lengths is conducted. It is shown that by deploying optimized interleavers for the combi

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