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LAYOUT TECHNIQUES IN MOSFETS



Morgan Claypool Publishers, United States, 2016. Paperback. Book Condition: New. 229 x 152 mm Language: English. Brand New Book ***** Print on Demand *****. This book aims at describing in detail the different layout techniques for remarkably boosting the electrical performance and the ionizing radiation tolerance of planar Metal-Oxide-Semiconductor (MOS) Field Effect Transistors (MOSFETs) without adding any costs to the current planar Complementary MOS (CMOS) integrated circuits (ICs) manufacturing processes. These innovative layout styles are based on pn junctions engineering...

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- Authored by Salvador Pinillos Gimenez
- Released at 2016



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