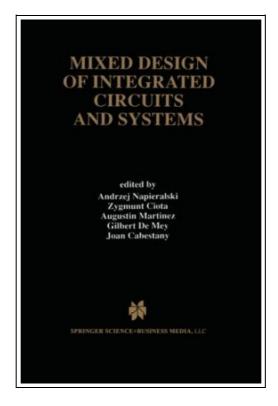
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MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS



Book Condition: New. Publisher/Verlag: Springer, Berlin | Very fast advances in IC technologies have brought new challenges into the physical design of integrated systems. The emphasis on system performance, in lately developed applications, requires timing and power constraints to be considered at each stage of physical design. The size of ICs is decreasing continuously, and the density of power dissipated in the circuits is growing rapidly. The first challenge is the Information Technology where new materials, devices, telecommunication and multimedia facilities are developed. The second one is the Biomedical Science and Biotechnology. The utilisation of bloodless surgery is possible now because of wide microsensors and micro-actuators application. Nowadays, the modern micro systems can be implanted directly into the human body and the medicine can be applied right in the proper time and place in the patient body. The low-power devices are being developed particularly for medical and space applications. This has created for designers in all scientific domains new possibilities which must be handed down to the future generations of designers. In this spirit, we organised the Fourth International Workshop "MIXED DESIGN OF INTEGRATED CIRCUITS AND SYSTEMS" in order to provide an international forum for discussion and the exchange of information on education, teaching experiences, training and technology transfer in the area of microelectronics and microsystems. | I: Analog Circuits Design. 1. Multichannel Low Noise, Low Power Analogue Readout Chip for Silicon Strip Detectors; W. Bialas, et al. 2. Design Methodology for Current Conveyor Based Continuous-Time Field-Programmable Analog Array; R. Grisel, et al. 3. RF Amp IC for Optical Disc Player; Chun-Sup Kim, et al. 4. CMOS Current Conveyor Design and Macromodel; S. Kuta, et al. 5. Behavioural Noise Modelling of Cross-Coupled Ring Oscillators; L.J. Opalski. 6. A 27 MHz Fully-Balanced OTA-C Filter in 2muM CMOS Technology; B. Pankiewicz, et...



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