

## Design for Yield and Reliability for Nanometer CMOS Digital Circuits



Design for Yield and Reliability for Nanometer CMOS Digital Circuits  
Statistical design, soft error modeling, Adaptive body bias, Negative capacitance circuits

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### Book Review

Simply no phrases to describe. It is actually rally interesting through reading time period. Your lifestyle period will probably be transform the instant you complete reading this article book.

(Rowland Bauch)

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